

Notice of References Cited	Application/Control No. 10/786,406	Applicant(s)/Patent Under Reexamination LEE, IK-JOO	
	Examiner Chun Cao	Art Unit 2115	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,559,738 A	09-1996	Mizobata et al.	365/189.01
*	B	US-6,172,935 B1	01-2001	Wright et al.	365/233
*	C	US-6,175,905 B1	01-2001	Manning, Troy A.	711/169
*	D	US-6,363,454 B1	03-2002	Lakhani et al.	711/100
*	E	US-6,414,894 B2	07-2002	Ooishi et al.	365/222
*	F	US-6,462,997 B2	10-2002	Sugamoto et al.	365/203
*	G	US-6,798,707 B2	09-2004	Niwa et al.	365/221
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Venkatesh et al., "TP 2.7: A 55ns 0.35um 5V-only 16 M Flash Memory with Deep-power-Down", 1996 IEEE international Solid State Circuit Conference, Digest of Technical Papers, Pages 44-45, 415.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.